

Active

- L1: (129517) transient
- L2: (481820) random
- L3: (679) 1 with 2
- L4: (89) generat\$3 with 3
- L5: (6) logic\$2 with 4
- L6: (18) 2.ti. and 4
- L7: (33) 2.ab. and 4
- L8: (386401) feedback or feed adj back
- L9: (40092) switch\$3 with 8
- L10: (153) 9 with 2
- L11: (0) 4 and 10
- L12: (12465) random near5 generate
- L13: (8) 9 with 12
- L14: (64) ((feedback or feed adj back) near5 invert\$3) and (random near5 generate)
- L15: (4) 10 and 14

Failed

- (0) '

Saved

- (643261) invert\$3
- (16299) (feedback or feed adj back) near5 invert\$3
- (2) ((feedback or feed adj back) near5 invert\$3) same (random near5 generate)
- (0) 6.ti. and ((feedback or feed adj back) near5 invert\$3)
- (3) 6.ab. and ((feedback or feed adj back) near5 invert\$3)

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DBs: USPAT: US-PGPUB; EPO; JPO; DERWENT; IBM; TDB

Pure

Default operator: OR

 Highlight all hit terms initially

BRD Form IS&R Form Image Text HTML

U	I	Document ID	Issue Date	Pages	Title	Current OR	Current XRef	Retrieval Cl	Inventor	S	C	P	2	3	4
1	<input type="checkbox"/>	US 6240432 B1	20010529	6	Enhanced random number generator	708/252			Chuang, Wei-Tong et al.	<input checked="" type="checkbox"/>					
2	<input type="checkbox"/>	US 5790626 A	19980804	17	Bi-directional linear feedback shift register	377/69	377/81		Johnson, David J. et al.	<input checked="" type="checkbox"/>					
3	<input type="checkbox"/>	US 6233205 B1	20010515	15	Built-in self test method for measuring clock to out delays	368/118	327/107; 327/263;		Wells, Robert W. et al.	<input checked="" type="checkbox"/>					
4	<input type="checkbox"/>	US 6356514 B1	20020312	15	Built-in self test method for measuring clock to out delays	368/113	368/118; 368/120		Wells, Robert W. et al.	<input checked="" type="checkbox"/>					
5	<input type="checkbox"/>	US 5596544 A	19970121	61	Very large scale integrated planar read only memory	365/233.5	326/94; 365/203;		Komarek, James A. et al.	<input checked="" type="checkbox"/>					
6	<input type="checkbox"/>	US 6522210 B1	20030218	20	Random pulse generator	331/78	331/46; 331/47;		Dvorak, Mark Daniel et al.	<input checked="" type="checkbox"/>					
7	<input type="checkbox"/>	US 20020118071 A	20020829	12	Circuit and method for generating a variable frequency	331/47	331/57; 331/78		Langston, Roland V.	<input checked="" type="checkbox"/>					
8	<input type="checkbox"/>	US 6507247 B2	20030114	11	Circuit and method for generating a variable frequency	331/47	331/57; 331/78		Langston, Roland V.	<input checked="" type="checkbox"/>					

Ready

NUM

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